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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/751,430 | 01/06/2004 | Akira Umezawa | 247289US2S | 3199 |

TITLE OF INVENTION: SEMICONDUCTOR MEMORY DEVICE INCLUDING MOS TRANSISTOR HAVING A FLOATING GATE AND A CONTROL GATE
07/14/2005 MBEYENE2 00000208 10751430

| 01 FC:1501 | 1400.00 OP | 02 FC:1504 | 300.00 OP | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|-------------|--------------|------------|-----------|-----------|-----------------|------------------|------------|
| APPLN. TYPE | SMALL ENTITY | | | \$1400 | \$300 | \$1700 | 08/10/2005 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|------------|----------|----------------|
| LAM, DAVID | 2827 | 365-189110 |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

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1 OBLON, SPIVAK,

2 McCLELLAND, MAIER

3 & NEUSTADT, P.C.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

KABUSHIKI KAISHA TOSHIBA

Tokyo, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Joseph Scattetta, Jr.

Typed or printed name

Date

JUL 12 2005
Reg. No. 26,803

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